

Notice of References Cited	Application/Control No. 10/038,689		Applicant(s)/Patent Under Reexamination RICH ET AL.	
	Examiner Akash Saxena		Art Unit 2128	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,875,111	02-1999	Patel, Dhimant	703/19
*	B	US-5,535,145	07-1996	Hathaway, David J.	703/2
*	C	US-6,915,252	07-2005	Li, Mu-Jing	703/22
*	D	US-5,870,309	02-1999	Lawman, Gary R.	716/6
*	E	US-6,038,384	03-2000	Ehrler, Timothy J.	716/6
*	F	US-6,553,550	04-2003	Menegay et al.	716/6
*	G	US-6,817,000	11-2004	Rich et al.	716/6
*	H	US-6,442,738	08-2002	Brehmer, Joseph J.	716/5
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	IEEE Std 1076.4-1995 - IEEE Standard for VITAL Application-Specific Integrated Circuit (ASIC) Modeling Specification - 1995 (relevant pages)			
	V	IEEE Std 1481-1999 - IEEE Standard for Integrated Circuit (IC) Delay and Power Calculation System - 1999 (relevant pages)			
	W	Standard Delay Format Specification - Version 2.1 - 1994 (relevant pages)			
	X	EE Times Article - 1996 - Use of equations in DCL.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/038,689	Applicant(s)/Patent Under Reexamination RICH ET AL.	
	Examiner Akash Saxena	Art Unit 2128	Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Standardizing ASIC Libraries in VHDL Using VITAL: a Tutorial"; Krolkoski S.J.; IEEE 1995 Custom Integrate Circuits Conference.
	V	"Modeling ASIC Memories in VHDL"; Balaji E. et al; EURO DAC with WEURO VHDL 1996, IEEE.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.